

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT APPLICATION of: PARK

Confirmation No.: 9322

Application No.: 10/626,549

Group Art Unit: 2811

Filed: July 25, 2003

Examiner: Unassigned

Title: METHOD FOR MANUFACTURING MOS TRANSISTOR AND
SEMICONDUCTOR DEVICE EMPLOYING MOS TRANSISTOR MADE USING
THE SAME

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May 20, 2004

REQUEST FOR APPROVAL OF DRAWING CORRECTIONS

Mail Stop Non-Fee Amendment
Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Notice of Omitted Items, dated March 23, 2004, Applicant respectfully requests approval of this Request for Approval of Drawing Correction for purpose of renumbering Figs. 1C and 1D as Figs. 1A and 1B, respectively. The proposed drawing corrections are shown in red. No new matter is added. Applicant respectfully requests approval of the proposed drawing correction.

Respectfully submitted,

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GTB/CFL
Attachments: Figs. 1C and 1D

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